

PATENT NUMBER

703869

U.S. UTILITY Patent Application

G.I.P.E. SCANNED <u>mit</u> <u>W008</u> Q.A.	PATENT DATE
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APPLICATION NO. 09/702870	CONT/PRIOR F	CLASS 257	SUBCLASS G610	ART UNIT 2811 2827	EXAMINER J. P. P.
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TITLE: APPLICANTS

Keiichi Den

Semiconductor device of chip-on-chip structure

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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